TECHNICAL QUESTIONS

1. The output of a JK flipflop with asynchronous preset and clear inputs is ‘1’. The output can be changed to ‘0’ with one of the following conditions.
2. By applying j=0, k=0 and using a clock
3. By applying j=1, k=0 and using the clock
4. By applying j=1, k=1 and using the clock
5. By applying a synchronous preset
6. The total power dissipated by a transistor is a product of collector current and
7. Supply voltage
8. 0.7 v
9. Base-emitter voltage
10. Collector-emitter voltage
11. Buffer amplifier needs to have
12. Low input resistance and low output resistance
13. High input resistance and high output resistance
14. Low input resistance and high output resistance
15. High input resistance and low output resistance